

PATENT**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Jung-Chien Chang	Docket No.:	39524.0069
Serial No.:	10/615,139	Group Art Unit:	1762
Filing Date:	July 9, 2003	Examiner:	Lin, James
TITLE:	A METHOD FOR PREPARING THIN INTEGRATED CIRCUITS WITH MULTIPLE CIRCUIT LAYERS		
		Confirmation No.:	8097

AMENDMENT AND RESPONSE TO OFFICE ACTION**INTRODUCTORY COMMENTS**

This is a response to the Office Action, dated April 18, 2006, in connection with U.S. Patent Application No. 10/615,139. Please consider the amendments and the remarks as set forth below. **This Response is being timely filed within 3 months of the mailing date of the Office Action, so that no extension fee applies. If it is determined that there is a further fee associated with this Response, the United States Patent and Trademark Office is requested to consider this Response (with a petition if necessary) and charge the appropriate fee to Deposit Account No. 19-2814.**

Amendments to claims begin on page 2 of this paper.

Remarks begin on page 7 of this paper.